

First Named Inventor	Ebrahim Abedifard	<b>INFORMATION DISCLOSURE STATEMENT</b>
Serial No.	Unknown	
Filing Date	Herewith	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Attorney Docket No.	400.063US04	
Title: FLASH CELL WITH TRENCH SOURCE-LINE CONNECTION		

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with 37 C.F.R. §§ 1.56 and 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application. Applicant respectfully requests that this Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

Further pursuant to MPEP §609, because all of the references listed on the attached Form 1449 have been previously submitted and made of record in the parent application, U.S. Patent Application 09/741,525, filed December 19, 2000, copies of the references previously made of record in the parent application are not submitted herewith. Pursuant to 37 C.F.R. 1.98 (a)(2)(i), as this application was filed after June 30, 2003, Applicant has not included copies of U.S. Patents or U.S. Patent Applications. If the Examiner has any questions or concerns regarding this application, please contact the undersigned at (612) 312-2208.

Respectfully submitted,

Date: 05-19-04



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Applicant(s)	Ebrahim Abedifard	<b>INFORMATION DISCLOSURE STATEMENT FORM PTO-1449</b>
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Title: FLASH CELL WITH TRENCH SOURCE-LINE CONNECTION		Sheet 1 of 2

U.S. Patent References				
Examiner Initials	Document No.	Issue/Published Date	Name	Filing Date
	4,816,884	03/28/89	Hwang	07/20/87
	4,996,668	02/26/91	Paterson	02/20/90
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	5,469,383	11/21/95	McElroy	06/24/94
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	6,215,158	04/10/01	Choi	09/10/98

Examiner Signature		Date Considered	
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

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Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	T?
	Country	No.			
Other References					
Examiner Initials	Author, Title, Date, Pages, etc.				
	Micron Semiconductor Products, Inc., "2Mb, Smart 5 BIOS-Optimized Boot Block Flash Memory," <u>Flash Memory</u> www.micron.com, copyright 2000, Micron Technology, Inc., pp. 1-12				

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